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(74) Agent: **KANGXIN PARTNERS, P.C.**; Floor 16, Tower A, InDo Building, A48 Zhichun Road, Haidian District, Beijing 100098 (CN).

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(71) Applicant (for all designated States except US): **APEX-ONE MICROELECTRONICS LTD.** [—/CN]; Building 18, No. 115, Lane 572 Bibo Road, Zhangjiang Hi-Tech Park, Shanghai 201203 (CN).

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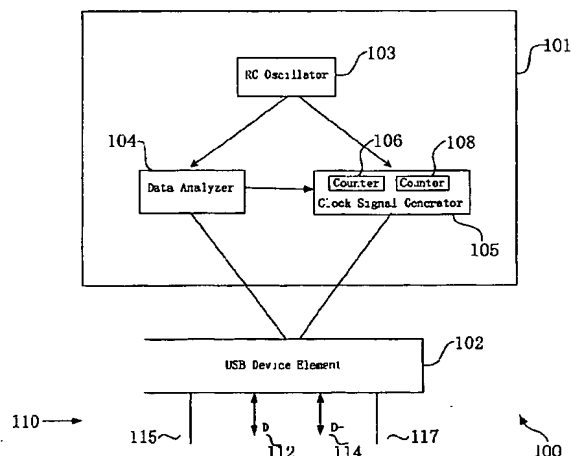
(72) Inventors; and

(75) Inventors/Applicants (for US only): **MA, Qingjiang** [CN/CN]; Room 202, No. 7, Lane 455 Fanghua Road, Shanghai 201204 (CN). **GAO, James, Y.** [CN/CN]; Room 1504, No. 5, Lane 366 Pucheng Road, Shanghai 200120 (CN). **REN, Yongqing** [CN/CN]; Room 501, No. 10, Lane 161 Rushan Road, Shanghai 200120 (CN).

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(54) Title: SYSTEM AND METHOD FOR CLOCK SIGNAL SYNCHRONIZATION



(57) Abstract: A system (101) for clock signal synchronization includes a data analyzer (104) and a synchronized clock signal generator (105) coupled to an RC oscillator (103). The data analyzer (104) generates a digital control signal representing the number of cycles of a reference signal of the RC oscillator (103) during an eight-bit period of an incoming token packet. The synchronized signal clock generator (105) uses the digital control signal to lock a clock signal to packets that have the same bit rate as the token packet.

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